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## TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number	10/784,417
Filing Date	February 23, 2004
First Named Inventor	Minchang Liang
Art Unit	2822
Examiner Name	Ida M. Soward
Attorney Docket Number	A1385

**ENCLOSURES (Check all that apply)**

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance communication to Group
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Remarks		
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**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm or Individual name

G. Victor Treyz

Signature

*G. Victor Treyz*

Date

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A1385

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Minchang Liang et al.  
Application No. : 10/784,417  
Confirmation No. : 1815  
Filed : February 23, 2004  
For : BIPOLAR TRANSISTORS WITH LOW BASE  
RESISTANCE FOR CMOS INTEGRATED CIRCUITS  
Group Art Unit : 2822  
Examiner : Ida M. Soward

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION

Sir:

This Reply is in response to the Office Action dated May 19, 2005 in connection with the above-identified patent application. Amendments to the claims are reflected in the listing of claims which begins on page 2. Remarks begin on page 7.